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| --- | --- | --- | --- | --- | --- |
| MIPS instruction | Functionality | Instructions | Machine code | Assembly instruction | Range |
| addi | Rx=Rx+imm | addval  Rx, imm | 10 xx iiii |  |  |
| slt | Rx=1 f Ry<Rx  Else Rx=0 | lt Rx, Ry,r2 |  |  |  |
| srl |  |  |  |  |  |
| beq | Rx==Ry  Pc=pc+imm  else pc++ | branch  Rx, Ry, imm |  |  |  |
| lw | Rx=Mem[imm] | Load Rx, imm | 10 xx iiii |  |  |
| sw | Mem[imm]=Rx | store Rx,imm | 0000 xx i |  |  |
| j | imm [-16, 9] | jump imm | 110 iiiii |  |  |
| andi |  |  |  |  |  |
| xor |  |  |  |  |  |