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| --- | --- | --- | --- | --- | --- |
| MIPS instruction | Functionality | Instructions | Machine code | Assembly instruction | Range |
| addi | Rx=Ry+imm | addval  Rx, Ry, imm |  |  |  |
| sub | Rx=Ry-r2 | Sub Rx,Ry,r2 |  |  |  |
| slt | Rx=1 f Ry<Rx  Else Rx=0 | lt Rx, Ry,r2 |  |  |  |
| srl |  |  |  |  |  |
| beq | Rx==Ry  Pc=pc+imm  else pc++ | branch  Rx, Ry, imm |  |  |  |
| lw | Rx=Mem[imm] | Load Rx, imm |  |  |  |
| sw | Mem[imm]=Rx | store Rx,imm |  |  |  |
| j |  | jump imm |  |  |  |
| andi |  |  |  |  |  |
| xor |  |  |  |  |  |
| (this one I can’t see from the picture I took sorry) |  |  |  |  |  |
| addu |  |  |  |  |  |